

Modbus communication Instructions



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**The xLogic SuperRelay is an
Easy Electronic Co., Ltd
manufactured product.**

Communication protocol between xLogic and HMI

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This communication protocol adopts MODBUS protocol. Any operation on PLC data, such as acquisition data from PLC or write data to PLC, and control etc. must be in accordance with this communication protocol format, besides connecting hardware and communication parameters setting shall match each other between PLC and HMI, otherwise, PLC cannot normally respond.

1. Communication Mode

At present, xLogic can only be set-up to communicate on standard Modbus networks using the transmission mode: RTU. Users select this mode, along with the serial port communication parameters (baud rate, parity mode, etc.), during configuration of each controller. The mode and serial parameters must be the same for all devices on a Modbus network.

RTU Mode

| | | | | | | | |
|---------|---------------|-------------|--------|-----|--------|--------------------|---------------------|
| Address | Function code | Data Number | Data 1 | ... | Data n | CRC low-order byte | CRC high-order byte |
|---------|---------------|-------------|--------|-----|--------|--------------------|---------------------|

PLC mode selection : MODBUS RTU

Communication parameter set :

Baud rates : 9600

Data bit : 8

Stop bit : 1

Checkout mode: Non parity checking

The selection of RTU mode pertains only to standard Modbus networks. It defines the bit contents of message fields transmitted serially on those networks. It determines how information will be packed into the message fields and decoded.

On other networks like MAP and Modbus Plus, Modbus messages are placed into frames that are not related to serial transmission.

RTU Framing

In RTU mode, messages start with a silent interval of at least 3.5 character times. This is most easily implemented as a multiple of character times at the baud rate that is being used on the network (shown as T1–T2–T3–T4 in the figure below). The first field then transmitted is the device address.

The allowable characters transmitted for all fields are hexadecimal 0–9, A–F. Networked devices monitor the network bus continuously, including during the ‘silent’ intervals. When the first field (the address field) is received, each device decodes it to find out if it is the addressed device. Following the last transmitted character, a similar interval of at least 3.5 character times marks the end of the message. A new message can begin after this interval.

The entire message frame must be transmitted as a continuous stream. If a silent interval of more than 3.5 character times occurs before completion of the frame, the receiving device flushes the incomplete message and assumes that the next byte will be the address field of a new message. Similarly, if a new message begins earlier than 3.5 character times following a previous message, the receiving device will consider it a continuation of the previous message. This will set an error, as the value in the final CRC field will not be valid for the combined messages.

A typical message frame is shown below.

| START | ADDRESS | FUNCTION | DATA | CRC CHECK | END |
|-------------|---------|----------|----------|-----------|-------------|
| T1-T2-T3-T4 | 8Bit | 8Bit | n ↑ 8Bit | 16Bit | T1-T2-T3-T4 |

How the Address Field is Handled

The address field of a message frame contains two characters (ASCII) or eight bits (RTU). Valid slave device addresses are in the range of 0 – 247 decimal. The individual slave devices are assigned addresses in the range of 1 – 247. A master addresses a slave by placing the slave address in the address field of the message. When the slave sends its response, it places its own address in this address field of the response to let the master know which slave is responding.

Address 0 is used for the broadcast address, which all slave devices recognize. When Modbus protocol is used on higher level networks, broadcasts may not be allowed or may be replaced by other methods.

How the Function Field is Handled

The function code field of a message frame contains two characters (ASCII) or eight bits (RTU). Valid codes are in the range of 1 – 255 decimal. Of these, some codes are applicable to all xLogic, while some codes apply only to certain models, and others are reserved for future use.

When a message is sent from a master to a slave device the function code field tells the slave what kind of action to perform. Examples are to read the ON/OFF states of a group of discrete coils or inputs; to read the data contents of a group of registers; to read the diagnostic status of the slave; to write to designated coils or registers; or to allow loading, recording, or verifying the program within the slave.

When the slave responds to the master, it uses the function code field to indicate either a normal (error-free) response or that some kind of error occurred (called an exception response). For a normal response, the slave simply echoes the original function code. For an exception response, the slave returns a code that is equivalent to the original function code with its most-significant bit set to logic 1.

The master device's application program has the responsibility of handling exception responses. Typical processes are to post subsequent retries of the message, to try diagnostic messages to the slave, and to notify operators.

Data Field

The data field is constructed using sets of two hexadecimal digits, in the range of 00 to FF hexadecimal. These can be made from a pair of ASCII characters, or from one RTU character, according to the network's serial transmission mode.

The data field of messages sent from a master to slave devices contains additional information which the slave must use to take the action defined by the function code. This can include items like discrete and register addresses, the quantity of items to be handled, and the count of actual data bytes in the field.

If no error occurs, the data field of a response from a slave to a master contains the data requested. If an error occurs, the field contains an exception code that the master application can use to determine the next action to be taken.

The data field can be non-existent (of zero length) in certain kinds of messages. For example, in a request from a master device for a slave to respond with its communications event log (function code 0B hexadecimal), the slave does not require any additional information.

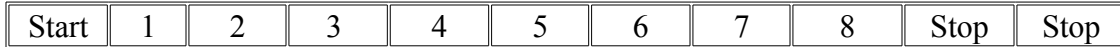
How Characters are Transmitted Serially

When messages are transmitted on standard Modbus serial networks, each character or byte is sent in this order (left to right):

Least Significant Bit (LSB) . . . Most Significant Bit (MSB)

With RTU character framing, the bit sequence is:

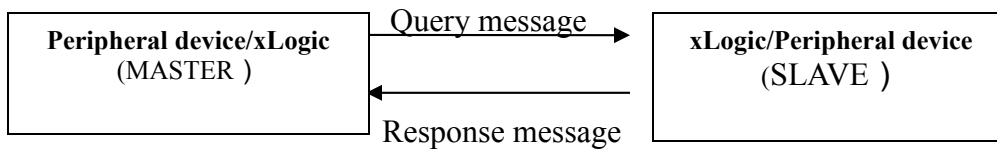
Without Parity Checking

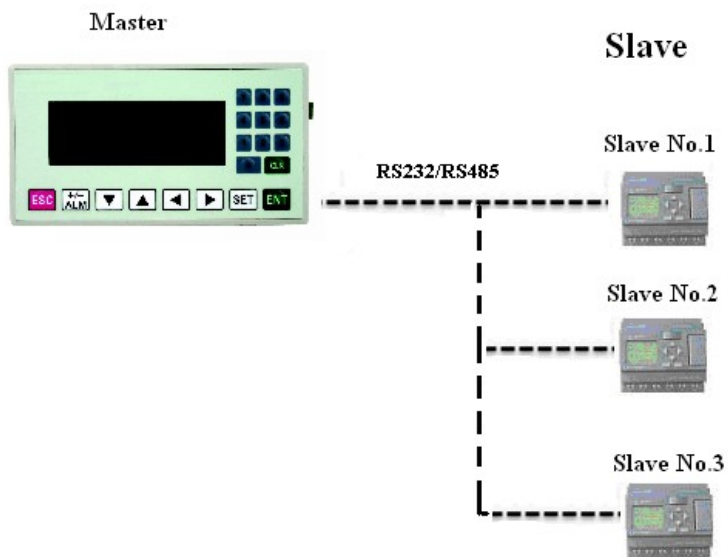
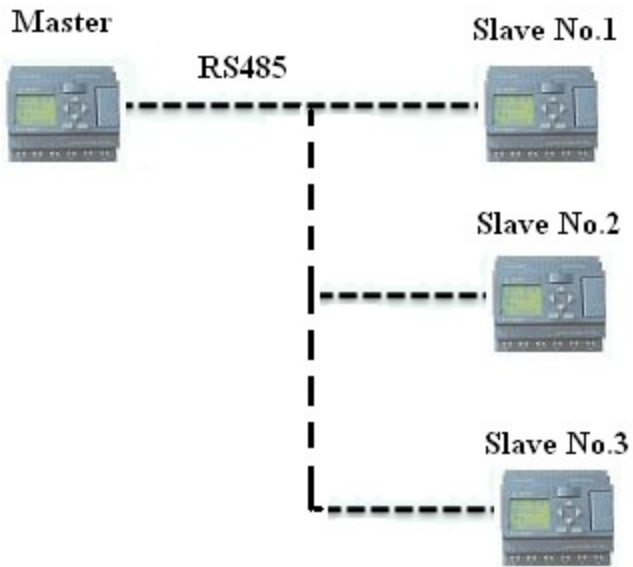


Bit Order (RTU)

2. It is optional for xLogic to be as a slave or master in a Modbus communication network.

As the following figure:

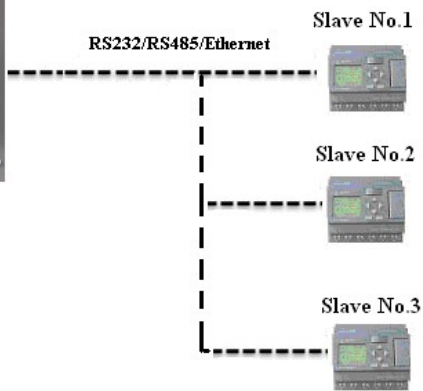




**PC (xLogicsoft, SCADA)
Master**



Slave



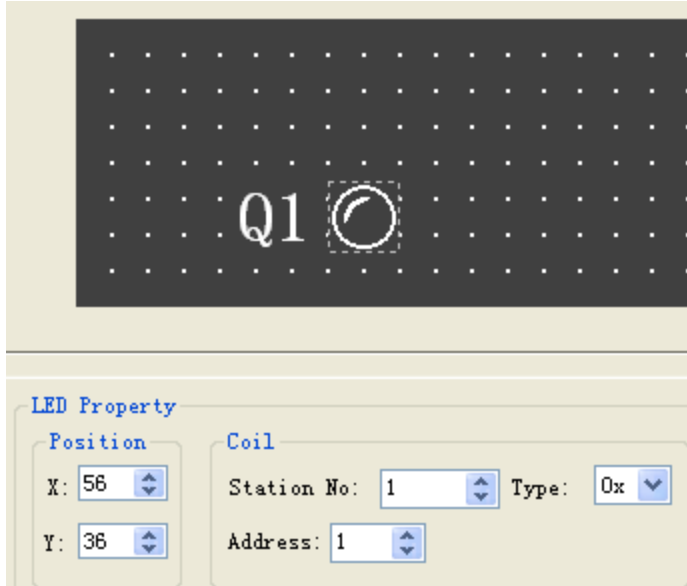
At present xLogic supports baud rate: 9600. The default is 9600. The default is non parity checking mode. MODBUS RTU is used as communication protocol of xLogic. The defaulted communication protocol is MODBUS RTU format .Default address: 1. Legal address range: 1~247.



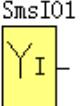
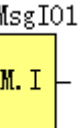

Notes:

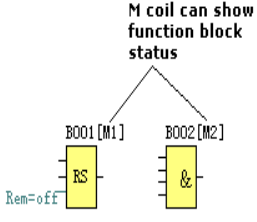
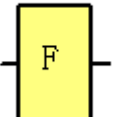
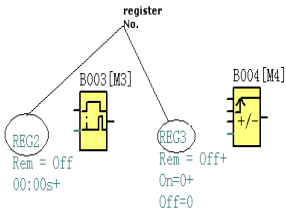
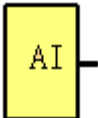
1. The Max length of frame command/order which xLogic supports is 40 characters (Excluding STX and ETX).
2. The address and baud rate of xLogic can be modified via the unit's keypad.
3. If xLogic serves as master, the blocks F, AF, Modbus Read, and Modbus Write would be used when programming.

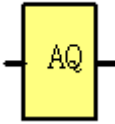
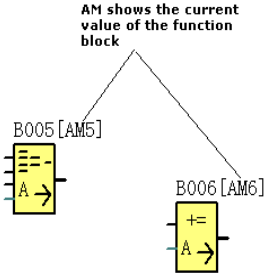

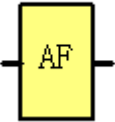
xLogic MODBUS Protocol Memory Map:

Note: All register addresses of xLogic start from 0, customers should plus 1 if start address is from 1 of third party devices. For example in MD204L configuration software the Q1 address should be 0x 1.



| Type | Unit | Address (DECIMAL) | Data Format | Attribute |
|--|--|---|----------------|----------------------|
| Digital Input Block in xLogicsoft:  Type (1x) | ELC-18 (CPU) ELC-E-16 (EXT1) ELC-E-16 (EXT2) ELC-E-16 (EXT3) . . . ELC-12 (CPU) ELC12-E-8 (EXT1) ELC12-E-8 (EXT2) ELC12-E-8 (EXT3) | 0 ~ 11 12 ~ 19 20 ~ 27 28 ~ 35 . . . 0 ~ 7 8 ~ 15 16 ~ 23 24 ~ 31 | BIT | READ |
| Cursor Keys  Type (1x) | C | 256 ~ 259 | BIT | READ |
| SMS Digital Input  Type (1x) | ELC-SMS-D-R (SMS I1 ~ I6) | 260 ~ 265 | BIT | READ |
| SMS Message Input  Type (1x) | ELC-SMS-D-R (MsgI1 – I10) | 266 ~ 275 | BIT | READ |
| Coil Digital Outputs  Type (0x) | ELC-18 (CPU) ELC-E-16 (EXT1) ELC-E-16 (EXT2) ELC-E-16 (EXT3) . . . ELC-12 (CPU) ELC12-E-8 (EXT1) ELC12-E-8 (EXT2) ELC12-E-8 (EXT3) SMS Digital Output SMS Msg Output | 0 ~ 5 8 ~ 15 16 ~ 23 24 ~ 31 . . . 0 ~ 7 8 ~ 15 16 ~ 23 24 ~ 31 512 ~ 515 516 ~ 525 | BIT | READ And WRITE |

| | | | | |
|---|-----|---|-----------------|----------------------|
| <p>Middle Coil</p> <p>M coil can show function block status</p>  <p>Type (0x)</p> | M | ELC12 Series: 256 ~ 1279 ELC18 Series: 256 ~ 511 | BIT | READ |
| <p>F Outputs</p> <p>F1</p>  <p>Type (0x)</p> | F | ELC12 Series: 1536 ~ 1599 ELC18 Series: 768 ~ 799 | BIT | READ And WRITE |
| <p>Holding Registers (Timers/Counter,etc.)</p>  <p>Type (4x)</p> | REG | ELC12 Series: 0 ~ 1023 ELC18 Series: 0 ~ 255 | LONG | READ |
| <p>Analogue Input</p> <p>AI001</p>  <p>Type (4x)</p> | AI | ELC-12 (CPU) ELC12-E-8 (EXT1) ELC12-E-8 (EXT2) ELC12-E-8 (EXT3) ELC-18 (CPU) ELC-E-16 (EXT1) ELC-E-16 (EXT2) ELC-E-16 (EXT3) | SIGNED SHORT | READ |

| | | | | |
|--|---|---|-------------------------|-------------------------------|
| <p>Analogue Output</p> <p>AQ001</p>  <p>Type (4x)</p> | <p>AQ</p> <p>ELC-12 (CPU) ELC12-E-8 (EXT1) ELC12-E-8 (EXT2) ELC12-E-8 (EXT3)</p> <p>ELC-18 (CPU) ELC-E-16 (EXT1) ELC-E-16 (EXT2) ELC-E-16 (EXT3)</p> | <p>ELC12 Series: (1080 ~ 1535) 1080 ~ 1081 1082 ~ 1083 1084 ~ 1085 1086 ~ 1087</p> <p>ELC18 Series: (512 ~ 531) 512 ~ 513 514 ~ 515 516 ~ 517 518 ~ 519</p> | <p>SIGNED SHORT</p> | <p>READ And WRITE</p> |
| <p>Analogue Quantity Buffer</p> <p>AM shows the current value of the function block</p>  <p>Type (4x)</p> | <p>AM</p> | <p>ELC12 Series: 1536 ~ 2559</p> <p>ELC18 Series: 768 ~ 1023</p> | <p>SIGNED SHORT</p> | <p>READ</p> |
| <p>Threshold Trigger Value</p>  <p>Value (4x)</p> | <p>HEG</p> | <p>ELC12 Series: 2560 ~ 3071</p> <p>ELC18 Series: 1024 ~ 1279</p> | <p>WORD</p> | <p>READ</p> |
| <p>Analogue Flag</p> <p>AF1</p>  <p>Type (4x)</p> | <p>AF</p> | <p>ELC12 Series: 3072 ~ 3136</p> <p>ELC18 Series: 1280 ~ 1311</p> | <p>SIGNED SHORT</p> | <p>READ And WRITE</p> |

On the upper table host address range and xLogic Max address range are the same, and also different series PLC has different address range, hence user shall voluntarily pay more attention to host address range of the PLC being used. In case host address of communication order/command exceeds the address range of PLC being used, then such PLC would respond to ERROR 4 (illegal address), and simultaneously such command/order would not be executed by PLC being used.

Note:

1. 10 milliseconds would be regarded as the unit of Time for writing to the HMI.
2. One second would be regarded as the unit of Time for reading from the HMI.
3. The default address of xLogic is 1.
4. The total number of address being accessed should be less than the above table showing.

4 Explanation of communication order in detail

The following table contains some communication orders supported by xLogic.

| Order code (Hex) | Function description | Length of message(one frame order can deal with) | Remarks |
|------------------|--|--|--|
| 01 | Read one group coil status (00000 ~ 0XXXX) | -- | Read Coil Status (Output relay) |
| 02 | Fetch one group data of the status of switch input (10000 ~ 1XXXX) | -- | Read input Status (input relay) |
| 03 | Read data of multi-holding register (40000 ~ 4XXXX) | -- | Read Holding Registers (Output register) |
| 05 | Force the switch status of single coil (00000 ~ 0XXXX) | 1 | Force Single Coil |
| 06 | Pre-set the data of single register (40000 ~ 4XXXX) | 80 | Set single output register |
| 15 | Force multi-coils on/off data (00000 ~ 0XXXX) | many | |
| 16 | Write multi-holding registers data (40000 ~ 4XXXX) | | |
| | | | |
| | | | |
| 19~4F | Reserved | | |

RTU Format

Note1: In data field, one byte stands for BIT (1 means ON, 0 means OFF). One byte (00 ~FF) would be used to represent “char” type register parameters. The “int” type register parameter can be expressed with two bytes (0000~FFFF). 4 bytes (00 00 00 00 ~ FF FF FF FF) can stand for “long” type register parameter. The high-order byte is appended first, followed by the low-order byte.

Note2: The Max length of command/order message sent to PLC by host can not exceed 80 bytes, otherwise PLC will not execute such order, also without responding to such command/order message, furthermore, host cannot allow the Max length of responding message from PLC to exceed 80 bytes, otherwise, PLC would return to ERROR 3 (command/order cannot be executed.) status.